

1. Features

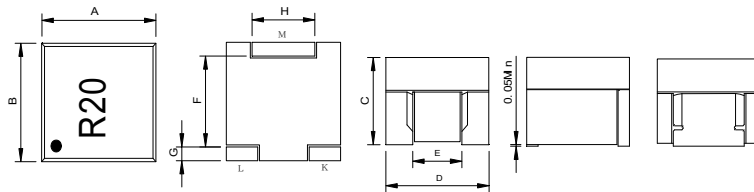
1. Low loss realized with low DCR.
2. High performance realized by metal dust core.
3. Ultra low buzz noise, due to composite construction.
4. 100% Lead(Pb)-Free & Halogen-Free and RoHS compliant.



2. Applications

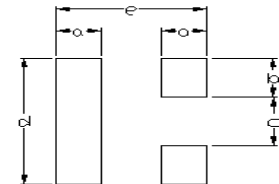
Commercial applications.

Recommend PC Board Pattern



A	B	C	D	E	F	G	H
6.20±0.2	6.20±0.2	5.15±0.15	6.10±0.3	2.60±0.3	5.20±0.3	0.45±0.1	3.50±0.2

Unit:mm



a	b	c	d	e
0.80	2.05	4.70	4.0	6.80

Note: 1. PCB layout is referred to standard IPC-7351B
 2. The above PCB layout reference only.
 3. Recommend solder paste thickness at 0.15mm and above.

4. Part Numbering

SLPI	0653	P3	-	R20	K	-	M405
A	B	C		D	E		F

A: Series
 B: Dimension
 C: Type
 D: Inductance
 E: Inductance Tolerance
 F: Code

BxC
 Standard
 R20=0.20uH
 K=±10%
 DC Resistance, Marking direction cannot decide polarity.

5. Specification

Part Number	Inductance L0A DC (uh)	I rms(A)	I sat (A) Typ		DCR (mΩ)
		Max	25℃	125℃	
SLPI0653P3-R20K-M405	0.20±10%	23	25	L23A≥0.14uH	0.4±0.05

Note:

1. Test frequency : L_s : 100KHz /0.1V.
2. All test data referenced to 20℃ ambient.
3. Testing Instrument(or equ) : Agilent 4284A, E4991A, 4339B, KEYSIGHT E4980A/AL, chroma3302, 3250, 16502.
4. Heat Rated Current (I_{rms}) will cause the coil temperature rise approximately ΔT of 40℃
5. Saturation Current (I_{sat}) will cause L0 to drop approximately 30%.
6. The part temperature (ambient + temp rise) should not exceed 125℃ under worst case operating conditions. Circuit design, component, PCB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.
7. I_{rms} Testing : Temperature rise is highly dependent on many factors including pcb land pattern, trace size, and proximity to other components.
 Therefore temperature rise should be verified in application conditions.
8. Rated DC current: The lower value of I_{rms} and I_{sat}.